

Utkarsh Girishkumar Patel**Phone:** +91-9409057017**Resi:**079-27430407**E-mail:**utkarshpatel1403@gmail.com
p11ec008@eced.svnit.ac.in**Address:**D-7,Part-4 ,Shayona City,
R.C.Technical Road,
Ghatolodia,
Ahmedabad-380061,
Gujarat,
India.**Profile/Objective**

Currently seeking an opportunity to exhibit the skills through a internship in a reputed organization in the field of Highly Integrated Photonics ,Lasers , Optical Computing ,Fiber Optics or Nanooptics .

Education

- Currently pursuing M.Tech In Electronics, Sardar Vallabhbhai National Institute of Technology, Surat (SVNIT, Deemed University) (Expected graduation date : May, 2013)
- Qualified GATE(Graduate Aptitude Test in Engineering) with 98.5 Percentile .
- Completed Graduation from Birla Vishwakarma Mahavidhyalaya(B.V.M) ,Vallabh Vidhyanagar,Aanad,(July 2010).

Fellowships

- Fellowship by Ministry of Human Resource and Development(MHRD) for qualifying GATE successfully and Pursuing masters from a Institute of National Importance (N.I.T) during (July 2011-present).

Industrial and Research Experiences

- Had a Summer Industrial experience in the field Telecom Technologies in Regional Telecom Training Center (RTTC),Ahmedabad,BSNL during June-July,2011
- Had an Industrial Research Experience in the field of Digital Displays and Embedded Systems in Shree Archi Embedded Solutions under Er. Hitesh Vadgasiya and Er. Nirghosh Patel during Jan-June, 2011. Also worked in the group of PCB Designing in same company
- Had an Industrial Research Experience in R&D Group of Siliconiks in the field of embedded systems development & embedded system programming areas during July-Dec,2010

Languages known

- English — Fluent in reading, writing and speaking
- Hindi — Fluent in reading, writing and speaking
- Gujarati— Fluent in reading, writing and speaking
- Sanskrit—Fluent in reading , Learning grammatically correct writing and speaking

Interests and Activities

- Interested in organizing scientific events and workshops, actively involved in Centre for Human Resource and Management (CHRD, SVNIT, Surat).
- Actively worked as General Secretary in B.V.M and Founder of Lakshya(Technical Forum of B.V.M).

Hobbies

Updating Knowledge in Latest Innovations in Science , Making Creative artistic things , Reading devotional books ,football .

Technical Skills

- **Applied Optics** :- Photonics CAD , Electronics Workbench , Matlab , Simulink , SciLab , Raman Microscope (Renishaw) ,UV and IR Spectroscopy ,OSA
- **Embedded**:- Verilog,VHDL,Altium(PCB designing),AVR Studio and Keil(Microcontroller Programming),RTOS & uclinux Programming ,RFID (Feig Electronics)
- **General**:-Microsoft Office, DOS Applications, C, C++, Visual Basic ,Linux Shell programming, and HTML Languages

Strength

- My greatest strength is that I never fear following my Heart .
- Neither Marks nor Money never tempted me and this always helped me for carrying in-depth studies in the field which attracted me the most .
- My Adaptive nature is God grace to me and so, I can easily adapt to new environment and learn things at a rapid pace .
- I have always been an excellent team member.

B.TECH COURSES

All the Fundamental Courses for Electronics and Telecommunication Engineering were covered under Graduation Course .

BE Major Project :- Mini-computer using ARM(Final Semester,Jan-June,2010)

Guided By:-Asst. Prof. Hiren Patel(B.V.M Engineering College)
Er. Sunny Sindhi(Potent Embedded Solution)

BE Minor Projects :-

- Optical Character Reading (7th Sem)
- Electronic stick for Visually Impaired(University funded Project)
- Image-audio Encryption and RFID Attendance System (6th Sem)
- USART for Xilinx FPGA kit (5th Sem)
- Wireless Toy car and Home automation through DTMF(5th Sem)
- Automatic Speed Controller for Fan and Coolers (4th Sem)
- LED display Clock(3rd Sem)
- Various Autonomous Robots according to specification for different technical Festivals .(All 4 years)

M.TECH COURSES

- Advanced Optical Communication and Systems
- Optical Networks
- Nanoelectronics
- Advanced Digital Signal Processing
- RF Circuits And Systems
- Information Theory And Coding
- Wireless Communication
- Digital Image Processing

Personal Information:

- Date of Birth : 14th March,1989
- Nationality : Indian
- Gender : Male
- Marital Status : Single

References

1. Prof.Suparva Patnaik

Head, Electronics Department,
SVNIT, Surat

Email: ssp@eced.svnit.ac.in

3.Prof.Apoorva Shah

Head , IT Department ,
GCET Engineering College ,
Anand ,Gujarat

Email: shahapoorva@ymail.com

2. Dr. Vivekanad Mishra

Asst. Prof, Dept. In Electronics Dept.,
SVNIT, Surat

Email: vive@eced.svnit.ac.in

4.Er. Nilesh Jambucha

CEO , Siliconiks Pvt. Ltd.,
Vadodara ,
Gujarat .

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